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By N. Shlezinger, J. Whang, Y. C. Eldar, and A. G. Dimakis

|CONTRIBUTED PAPER| This article reviews leading strategies for designing systems whose operation combines domain knowledge and data via model-based deep learning in a tutorial fashion.

500 Resistive Neural Hardware Accelerators *By K. Smagulova, M. E. Fouda, F. Kurdahi, K. N. Salama, and A. Eltawil* |CONTRIBUTED PAPER| This article reviews several state-of-the-art manycore and multinode ReRAM-based compute-in-memory neural network accelerators and their pipeline designs, providing insight into limitations and future directions.

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By Y. Wang, Z. Sun, Y. Guan, and D. Xu |CONTRIBUTED PAPER| This article provides and overview of megahertz wireless power transfer systems from a power electronics perspective.

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On the Cover: This month's cover image highlights digital photography which is just one application where software-defined imaging can be beneficial.